

Attorney's Docket No.: 13361-059001 Client's Ref. No.: MP0359

OFFICIAL COMMUNICATION FACSIMILE:

OFFICIAL FAX NO: (571) 273-2885

Art Unit : 2816

Examiner: Jackson, Blane J

Number of pages including this page

Applicant: Swee-Ann Teo et al.

Serial No.: 10/738,368

Filed

: December 15, 2003

Title

: Sigma Delta Modulated Phase Lock Loop with Phase Interpolation

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet is Response to Notice of Allowance, Part B - Fee Transmittal, Comments on Examiner's Reasons for Allowance and Submission of Credit Card Payment, faxed this day of ___] ___ United States Patent and Trademark Office.

Date: 1/10/07

Reg. No. 40,048

Respectfully submitted.

Fish & Richardson P.C. 500 Arguello Street, Suite 500 Redwood City, California 94063 Telephone: (650) 839-5070

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Attorney's Docket No.: 13361-059001 / MP0359

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Swee-Ann Teo et al.

Art Unit :

2618

Serial No.: 10/738,368

Examiner:

Jackson, Blane J

Filed

: December 15, 2003

Confirmation No.:

9617 -

Title

Notice of Allowance Date: 10/10/06

: SIGMA DELTA MODULATED PHASE LOCK LOOP WITH PHASE

INTERPOLATION

MAIL STOP ISSUE FEE

Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF ALLOWANCE

In response to the Notice of Allowance mailed April 10, 2006, enclosed are a completed issue fee transmittal form PTQL-85b, Comments on Examiner's Reasons for Allowance and a submission of Credit Card Payment for \$1400 for the required fee.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Reg. No. 40,048

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50348804.doi:

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Signature

Suzanne M. Forno

Typed or Printed Name of Person Signing Certificate

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Swee-Ann Teo et al.

Art Unit :

2618

Serial No.: 10/738,368

Examiner:

Jackson, Blane J

Filed

Confirmation No.:

9617

: December 15, 2003

Notice of Allowance Date: 10/10/2006

Title

: SIGMA DELTA MODULATED PHASE LOCK LOOP WITH PHASE

INTERPOLATION

MAIL S'TOP ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

COMMENTS ON EXAMINER'S REASONS FOR ALLOWANCE

Applicant recognizes that in accordance with M.P.E.P. § 1302.14, the Examiner's reasons for allowance need not set forth all of the details as to why the claims are allowed. In the abovereferenced application, Applicant does not concede that the Examiner's stated reasons for allowance are the only reasons for which the claims are allowable. In particular, Applicant does not concede that all of the identified limitations—i.e., "the prior art made of record fails to teach a phase locked loop comprising a phase interpolator operable to receive one or more phase signals, each of the phase signals being delayed with respect to the output carrier signal, the phase interpolator operable to generate an interpolated output signal based in part on the dither control signal for controlling the frequency of the output carrier signal"—are included in each identified claim or necessary to distinguish over the prior art of record or to satisfy the requirements of 35 U.S.C. § 112. Furthermore, the claims may be patentable for other reasons. In addition, dependent claims are allowable on their own merits, and are allowable on the basis of a sub-combination of the recited features of the dependent claims and their respective base claims.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

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Suzanne M. Forno

Typed or Printed Name of Person Signing Certificate

Applicant: Swee-Ann Tco et al.

Serial No.: 10/738,368

Filed: December 15, 2003

Page : 2 of 2

Attorney's Docket No.: 13361-059001 / MP0359

Respectfully submitted,

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